Docket No.: W&B-INF-1908

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : BERND GOEBEL ET AL.

Filed : CONCURRENTLY HEREWITH

Title : METHOD FOR FABRICATING A SEMICONDUCTOR

CONFIGURATION

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

- U.S. Patent No. 5,240,875 (Tsou), dated August 31, 1993;
- U.S. Patent No. 4,958,206 (Teng et al.), dated September 18, 1990;
- U.S. Patent No. 5,376,225 (Wakabayashi et al.), dated December 27, 1994;
- U.S. Patent No. 5,444,007 (Tsuchiaki), dated August 22, 1995;
- U.S. Patent No. 5,126,288 (Nakagawa), dated June 30, 1992;
- U.S. Patent No. 3,908,262 (Stein), dated September 30, 1975;
- U.S. Patent No. 5,843,797 (luchi), dated December 1, 1998;
- U.S. Patent No. 5,909,044 (Chakravarti et al.), dated June 1, 1999;

U.S. Patent No. 5,360,758 (Bronner et al.), dated November 1, 1994;

European Patent Application EP 0 991 117 A2 (Gutsche), dated April 5, 2000;

European Patent Application EP 0 378 782 A2 (Tamai et al.), dated July 25, 1990;

European Patent Application EP 0 981 158 A2 (Bronner et al.), dated February 23, 2000;

PCT WO 00/16414 (Foley), dated March 23, 2000;

Widmann, D. et al.: "Technologie hochintegrierter Schaltungen" [Technology of Highly Integrated Circuits], Springer Verlag, 2nd Edition, 1996, p. 82, p. 178, p. 282, p. 339;

International Search Report, dated March 7, 2003.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted

LAURENCE A. GREENBERG REG. NO. 29,308

For Applicants

Date: September 30, 2003

Lerner and Greenberg, P.A. Post Office Box 2480 Hollywood, FL 33022-2480

Tel: (954) 925-1100 Fax: (954) 925-1101

/nt/kf

FORM PTO-1449 (SUBSTITUTE)				Attorney Docket No.: W&B-INF-1908 Appl. No.:				
U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE								
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Applicant: BERND GOEBEL ET AL.				
(37 CFR 1.98(b))				Filing Date: September 30, 2003 Group Art Unit:				
							r 	
EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILI DA	
	Α	5,240,875	8/31/93	Tsou				
	В	4,958,206	9/18/90	Teng et al.				
	С	5,376,225	12/27/94	Wakabayashi et al.				
	D	5,444,007	8/22/95	Tsuchiaki	:			
	Е	5,126,288	6/30/92	Nakagawa				
	F	3,908,262	9/30/75	Stein				
	G	5,843,797	12/1/98	luchi				
	Н	5,909,044	6/1/99	Chakravarti et al.				
	ı	5,360,758	11/1/94	Bronner et al.				
		FOREIG	SN PATE	NT DOCUMENT				
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRA	
	J	0 991 117 A2	4/5/00	Europe	01, 150			
	K	0 378 782 A2	7/25/90	Europe				
	L	0 981 158 A2	2/23/00	Europe				
	М	00/16414	3/23/00	WIPO				
	N							
OTHER	R DOC	CUMENTS (Inclu	uding Aut	hor, Title, Date, F	ertinent	Pages,	etc.)	
		Widmann, D. et al.: "Technologie hochintegrierter Schaltungen" [Technology of Highly Integrated Circuits], Springer Verlag, 2 nd Edition, 1996, p. 82, p. 178, p. 282, p. 339						
EXAMINER	1, .,		DATE CONSIDERED					
	•							